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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/775,324	02/11/2004	Mitsuhiro Nawashiro	T36-164402M/RS	8552
21254	7590 11/04/2005		EXAMINER	
	NTELLECTUAL PR OURTHOUSE ROAD	ERDEM, FAZLI		
SUITE 200			ART UNIT	PAPER NUMBER
VIENNA, V	A 22182-3817		2826	

DATE MAILED: 11/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/775,324	NAWASHIRO ET AL.			
Office Action Summary	Examiner	Art Unit			
	Fazli Erdem	2826			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).					
Status					
1) Responsive to communication(s) filed on 15 Au	igust 2005.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims					
4) ☐ Claim(s) 1-3,5-8 and 10-18 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) ☐ Claim(s) 6-8 is/are allowed. 6) ☐ Claim(s) 5 is/are rejected. 7) ☐ Claim(s) 1-3 and 10-18 is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or election requirement.					
Application Papers					
9) The specification is objected to by the Examiner.					
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.					
Priority under 35 U.S.C. § 119					
<u>.</u>					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachment(s)					
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 	4) Interview Summary (Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	(PTO-413) te atent Application (PTO-152)			

Application/Control Number: 10/775,324 Page 2

Art Unit: 2826

DETAILED ACTION

Claim Objections

1. Claim 1-3 and 10-18 objected to because of the following informalities: The last paragraph of claim 1 states that "internal electrodes....comprise larger than those of other internal electrodes of said four internal electrodes". The last part "of said four internal electrodes" is vague. Furthermore, specification discloses that the "other internal electrodes" to be wiring electrodes. Examiner suggest the removing of "of said four internal electrodes"

Allowable Subject Matter

2. Claims 6-8 allowed.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 1. Claims 5 rejected under 35 U.S.C. 103(a) as being unpatentable over Ishinaga (6,476,410) in view of Meng et al. (6,661,032) further in view of JP 62-62568

Regarding Claims 5, Ishinaga discloses a backside light emitting chip type light emitting element and insulating substrate therefor where in Figs. 2 and 4, it is disclosed a substrate 1 having a mount surface with first light emitting element 4a and second light

Application/Control Number: 10/775,324

Art Unit: 2826

emitting element 4b. Four internal electrodes 2al, 2ar, 2bl, 2br are revealed on the mounting surface of substrate. Likewise, four external electrodes 3al, 3ar, 3bl and 3br are revealed on the mounting surface of the substrate. Upper external electrodes and internal electrodes 3al, 2al, 2ar and 3ar are connected via light emitting element 4ar and wiring 5a. Lower external electrodes and internal electrodes 3bl, 2bl, 2br and 3br are connected via light emitting element 4b and wiring 5b.

Ishinaga fails to disclose the required polarity of internal electrodes specifically and the required chamfered structure. However, Meng et al. disclose a light emitting diode package structure having an electro-static protective diode where in Figs. 2 and 3, the required polarity for the electrodes are disclosed. Furthermore, JP 62-62568 discloses a light emitting diode display where in Fig. 1a the electrode corners are chamfered.

It would have been obvious to one of having ordinary skill in the art at the time the invention was made to include the required polarity configuration of the electrodes and the chamfered configuration in Ishinaga as taught by Meng et al. and JP 62-62568, in order to have a light emitting element with ease of manufacture.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fazli Erdem whose telephone number is (571) 272-1914. The examiner can normally be reached on M - F 8:00 - 5:00.

Application/Control Number: 10/775,324 Page 4

Art Unit: 2826

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272-1915. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

FE October 27, 2005

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800